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NISSAN CHEMICAL INDUSTRIES, LTD. Electronic Materials Research Laboratories

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EUVL Resist TWG, Nov. 1 2007

Sapporo, Japan

Brewer Science/NCI

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Outline

- Benefits of under layers for EUVL
- Roadmap
- Results of outgas testing



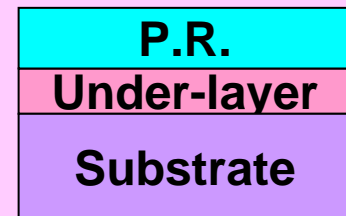
Under layers for EUV Lithography

Requirements and Benefits:

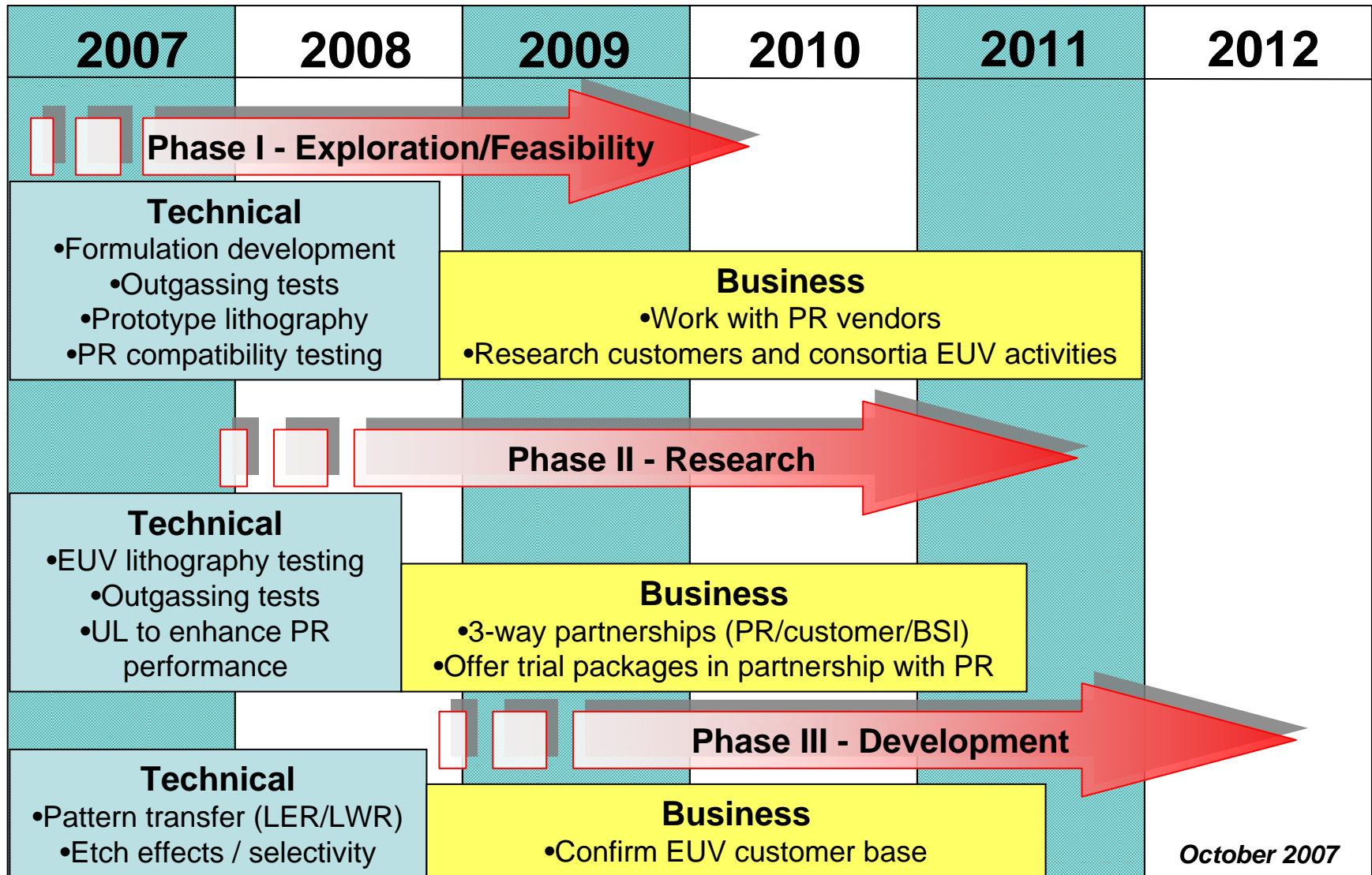
- High adhesion with PR
- Resist profile control
- Block substrate contamination (Barrier film)
- High etching rate
- Good performance with thin film (~30nm)
- Assist in pattern transfer
- Planarization

- **Minimal outgassing**
- **Photo speed enhancement**
- **Reduce LWR, LER**

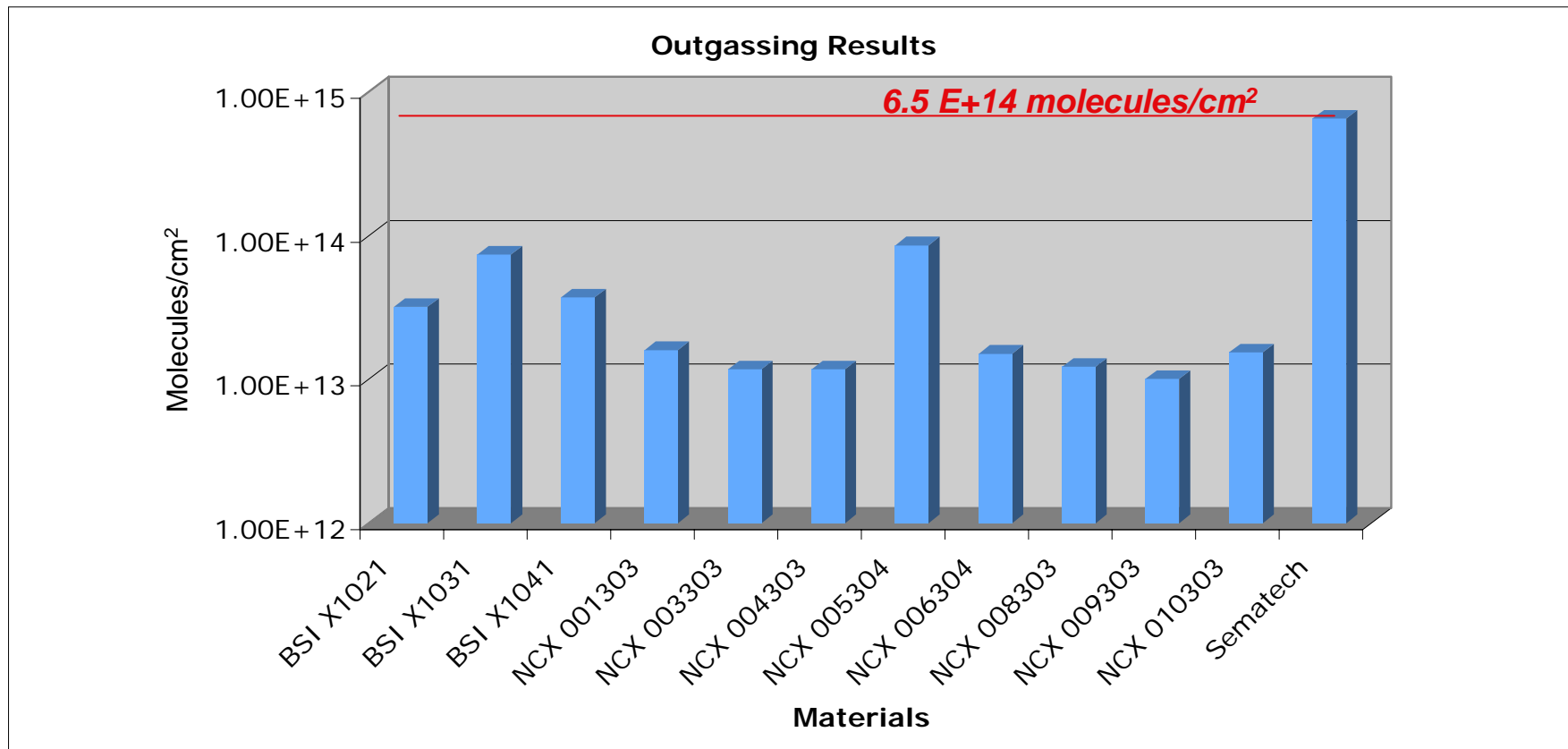
Stack structure:



EUV Underlayer Roadmap Beyond 32nm HP



Initial Outgassing Results



Order of Magnitude lower than Sematech spec

HM - organic and inorganic materials for EUVL





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